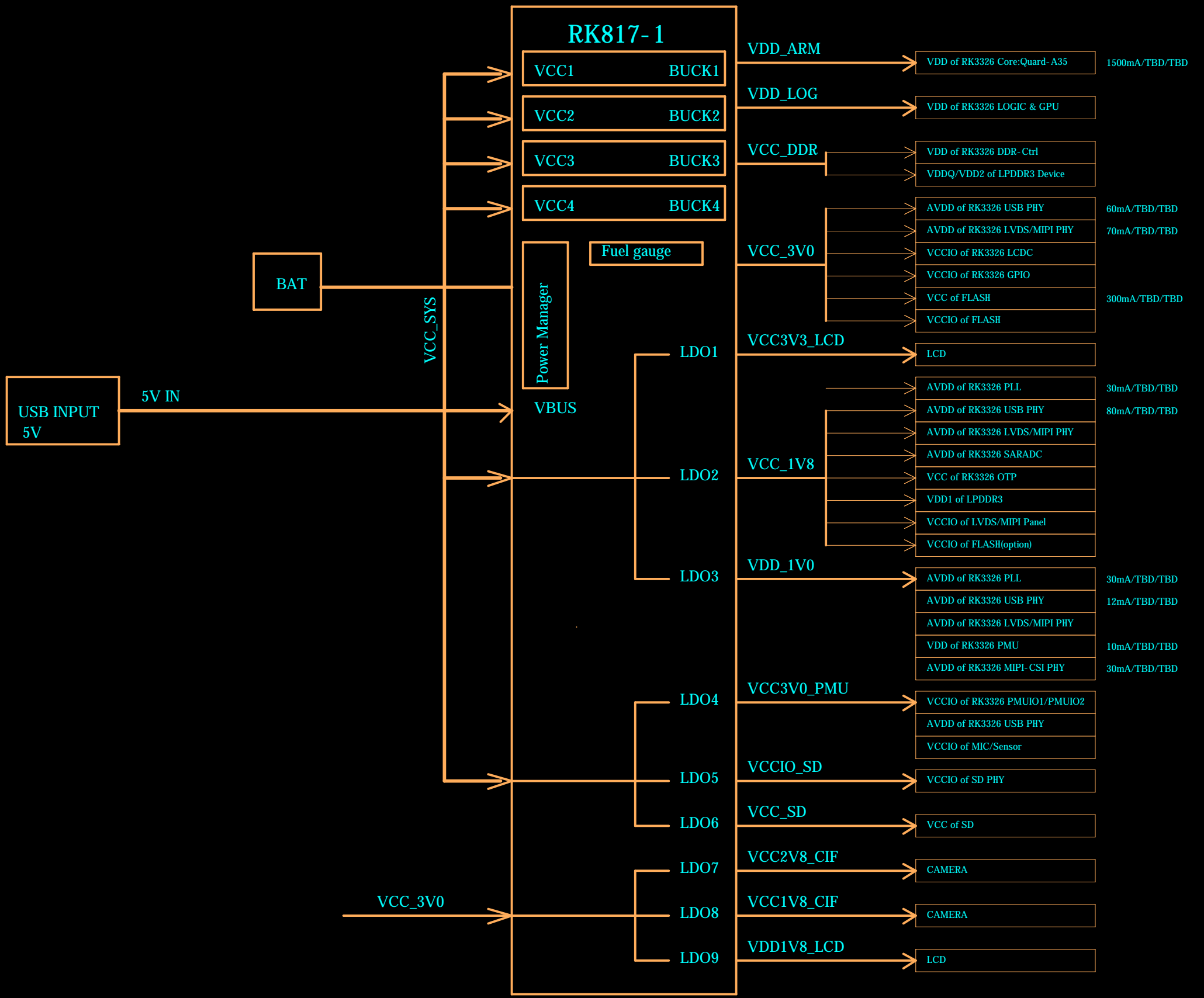
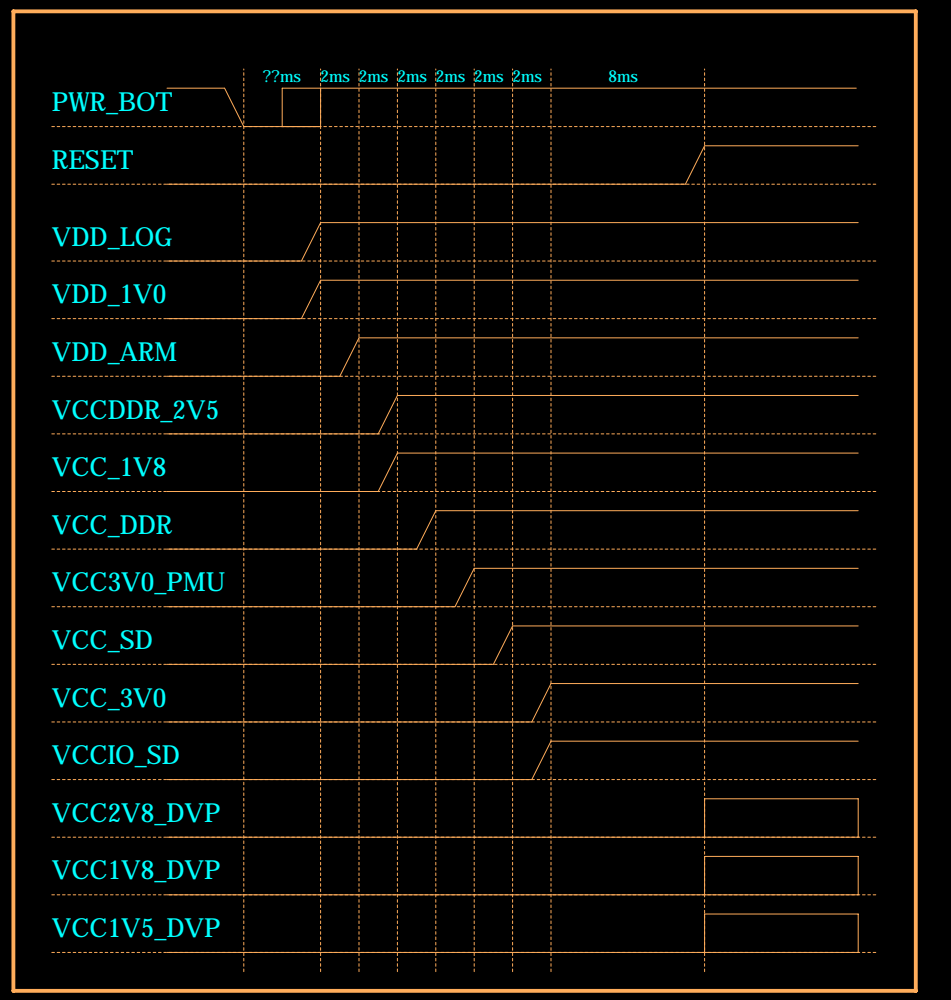


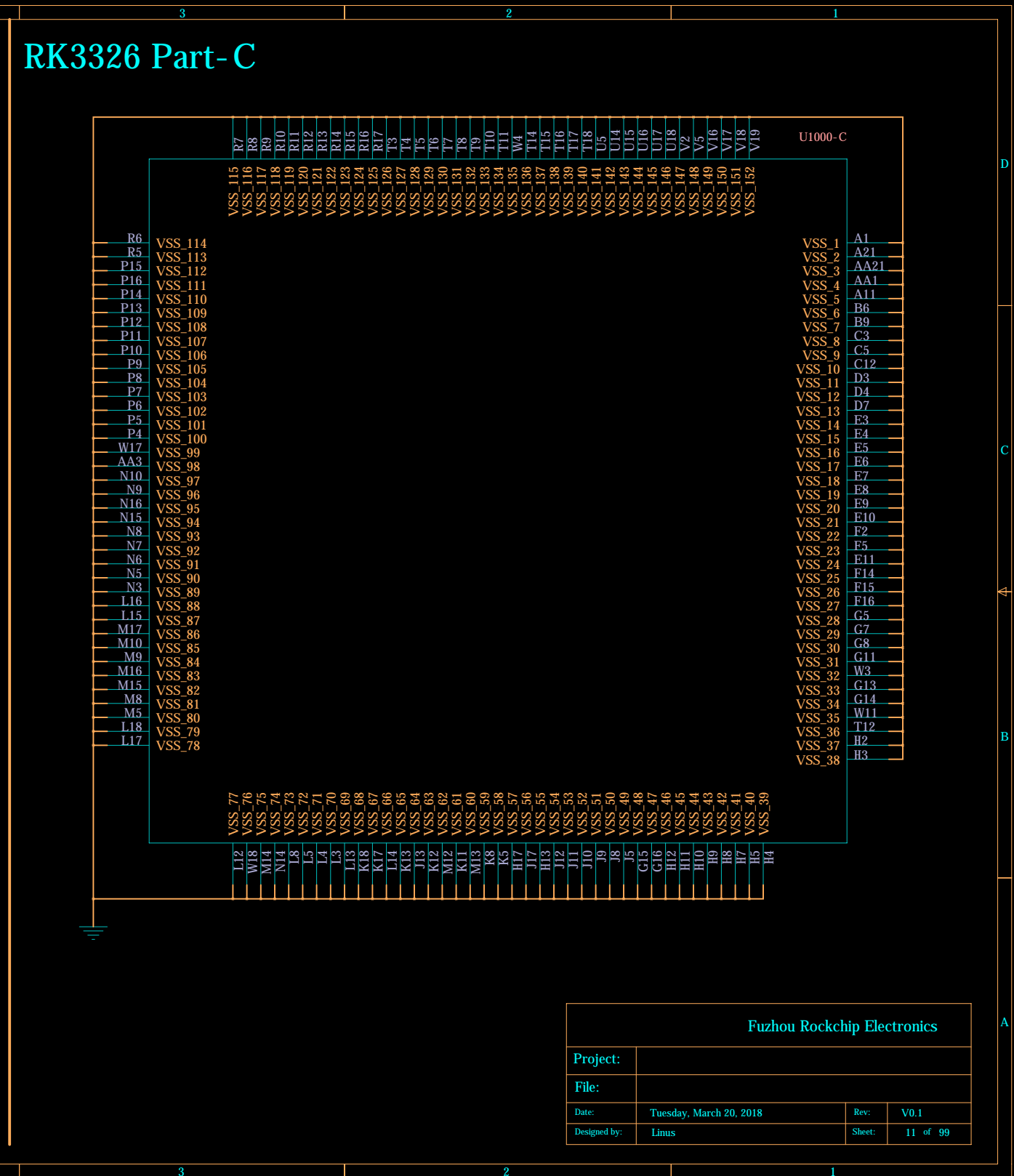
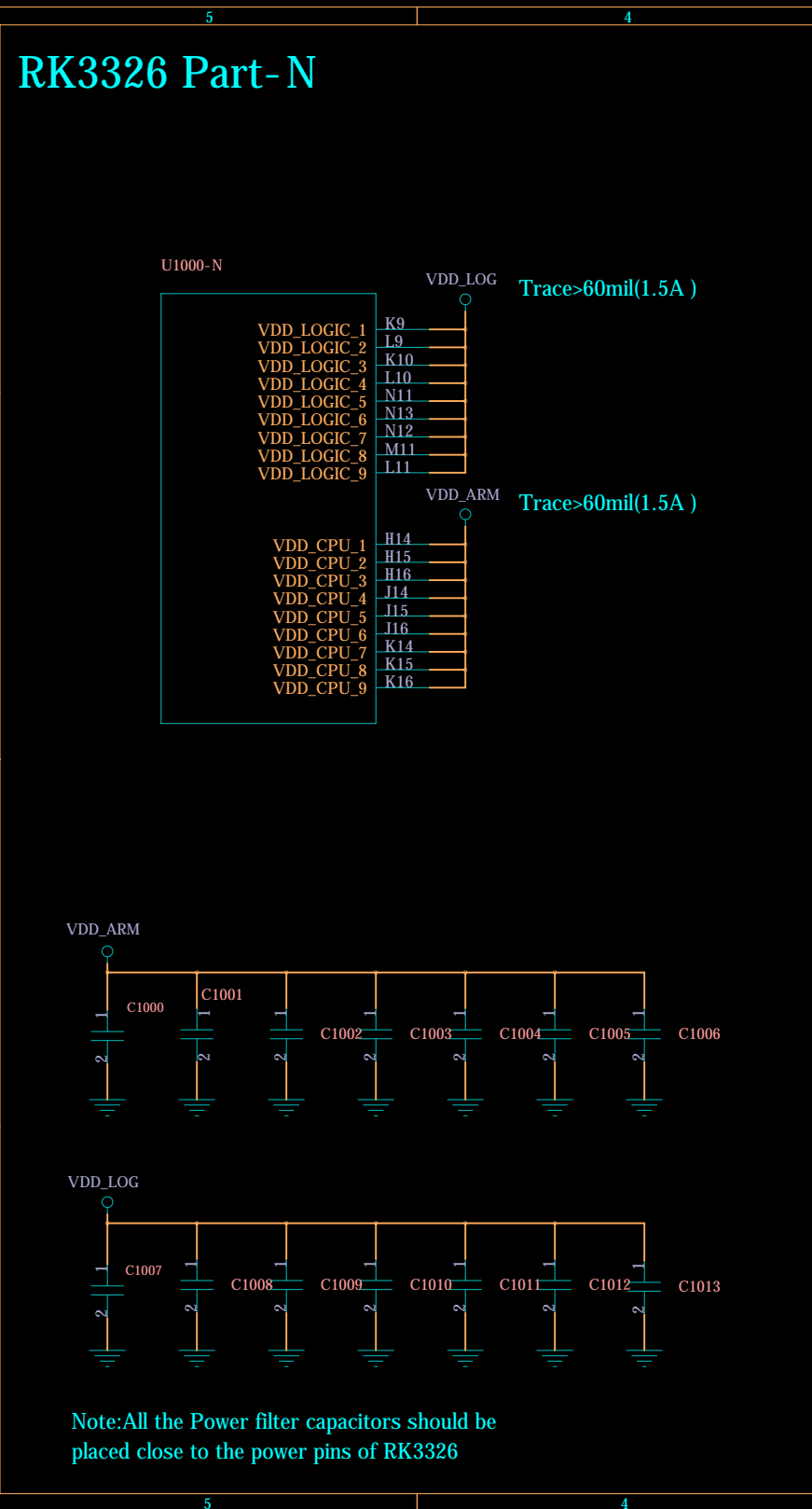
RK817-1 Power Diagram and Sequence



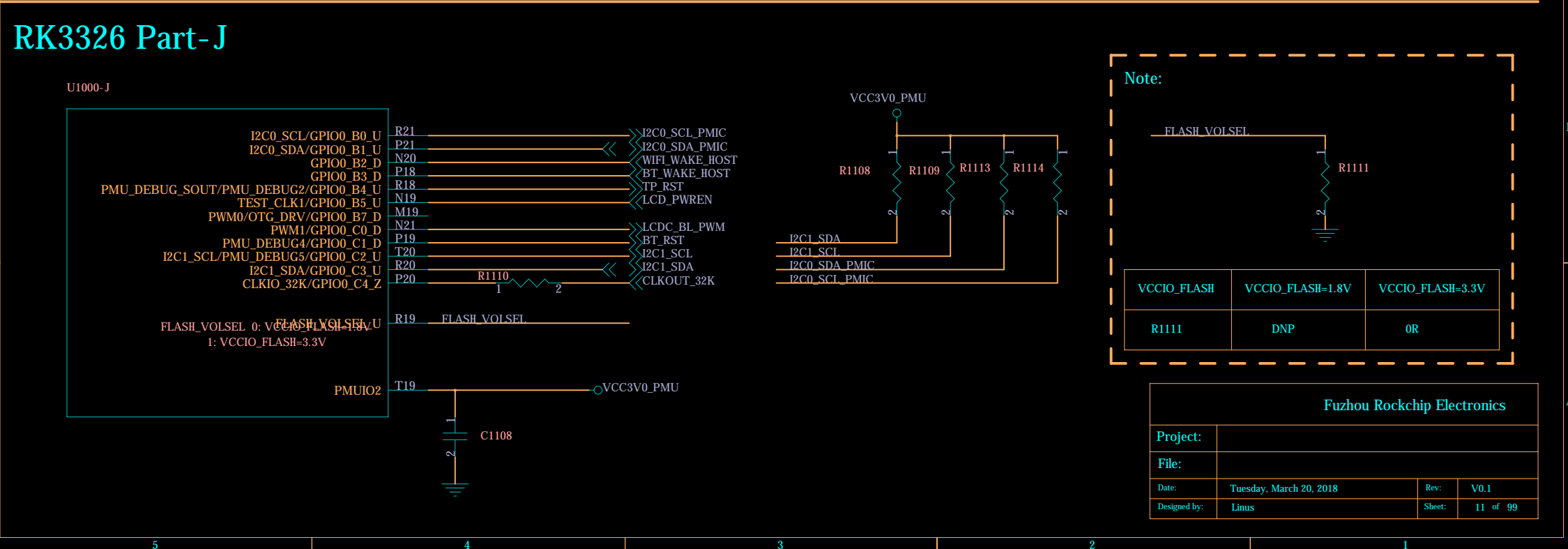
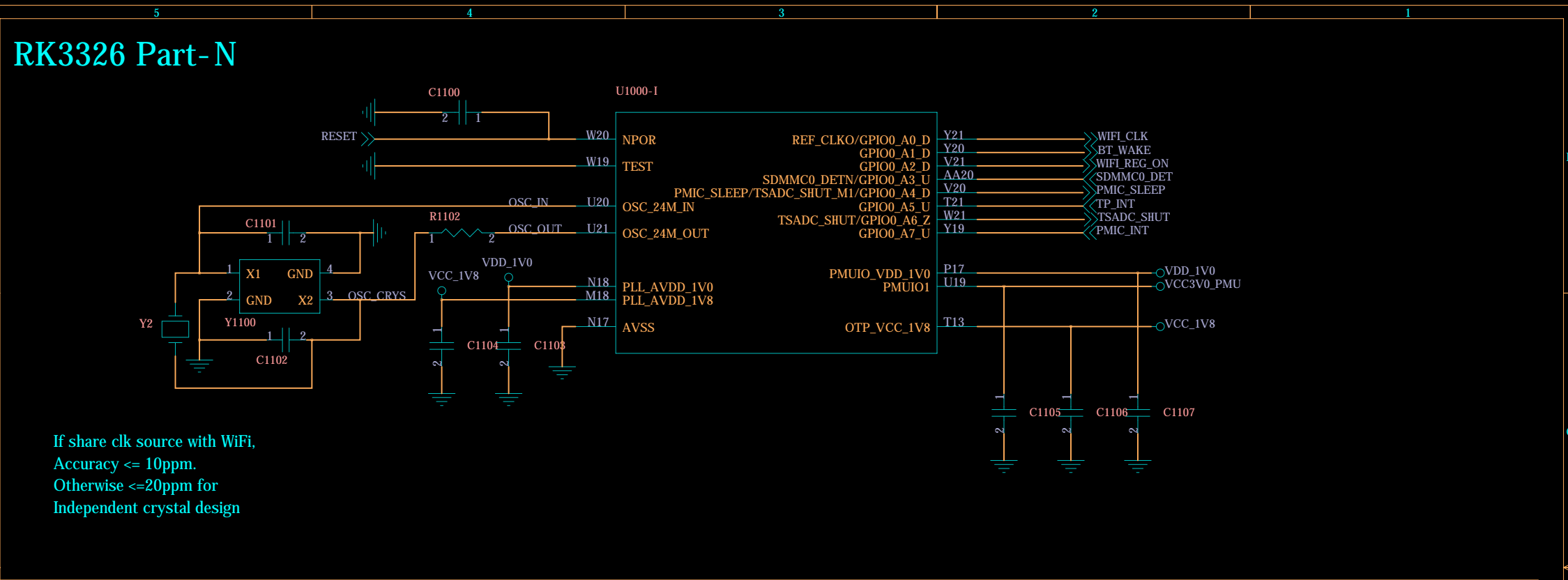
RK817-1 Power-on Sequence For RK3326

PowerName	PMIC Channel	Time Slot (step 2mS)	Default voltage	Supply Limit	Default ON/OFF	Sleep ON/OFF	Peak Current
VDD_ARM	BUCK1	Slot:2	1.0V	2.5A	ON	OFF	1.5A
VDD_LOG	BUCK2	Slot:1	1.0V	2.5A	ON	OFF	1.4A
VCC_DDR	BUCK3	Slot:4	FB-0.6V	1.5A	ON	ON	1.5A
VCC_3V0	BUCK4	Slot:7	3.0V	1.5A	ON	ON	1.5A
VCCDDR_2V5	LDO1	Slot:3	2.5V	500mA	ON	ON	
VCC_1V8	LDO2	Slot:3	1.8V	500mA	ON	ON	
VDD_1V0	LDO3	Slot:1	1.0V	500mA	ON	ON	
VCC3V0_PMU	LDO4	Slot:5	3.0V	100mA	ON	ON	12mA
VCCIO_SD	LDO5	Slot:7	3.0V	500mA	ON	ON	
VCC_SD	LDO6	Slot:6	3.0V	500mA	ON	ON	400mA
VCC2V8_DVP	LDO7		2.8V	500mA	OFF	OFF	
VCC1V8_DVP	LDO8		1.8V	500mA	OFF	OFF	
VCC1V5_DVP	LDO9		1.5V	500mA	OFF	OFF	
RESET	RESETB	Slot:11	OD				



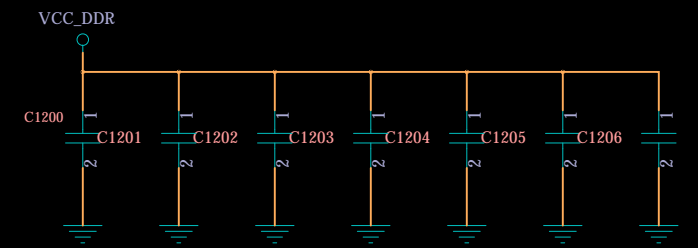


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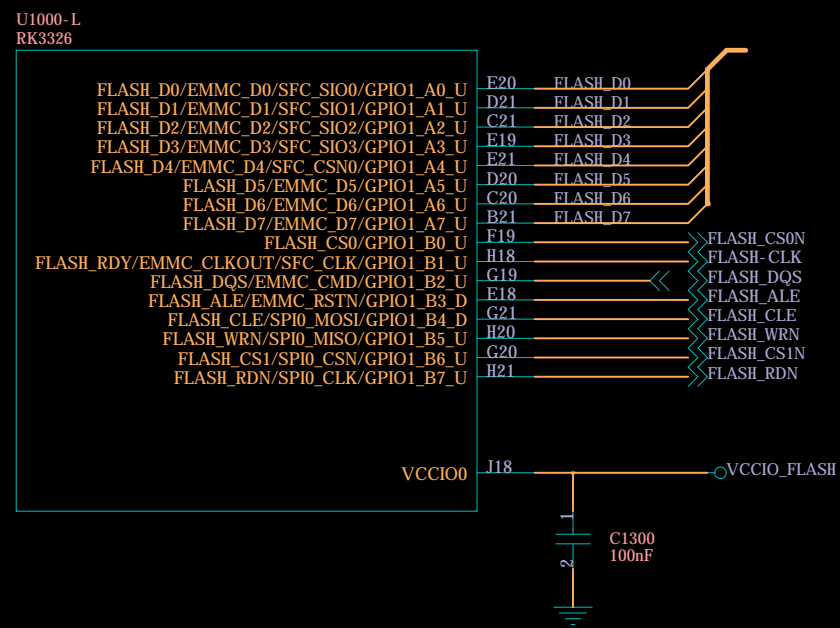
Power Filter



Note: All the Power filter capacitors should be placed close to the power pins of RK3326

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RK3326 Part-L

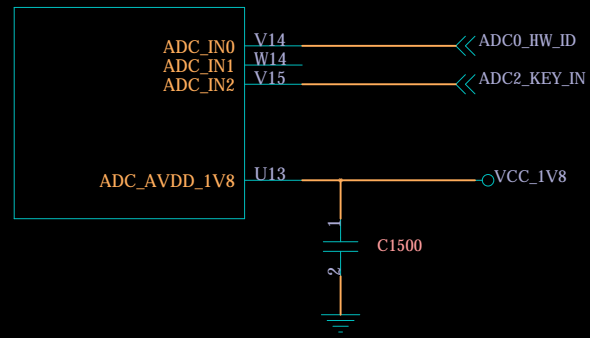


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RK3326 Part-F

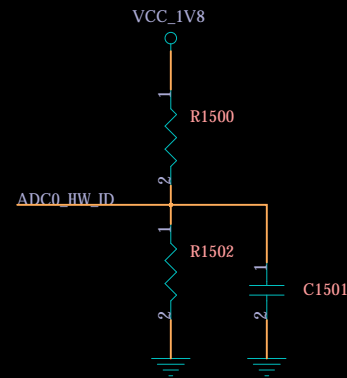
U1000-F



Key Name	SARADC
VOL+/RECOVERY	10
VOL-	170

HW ID

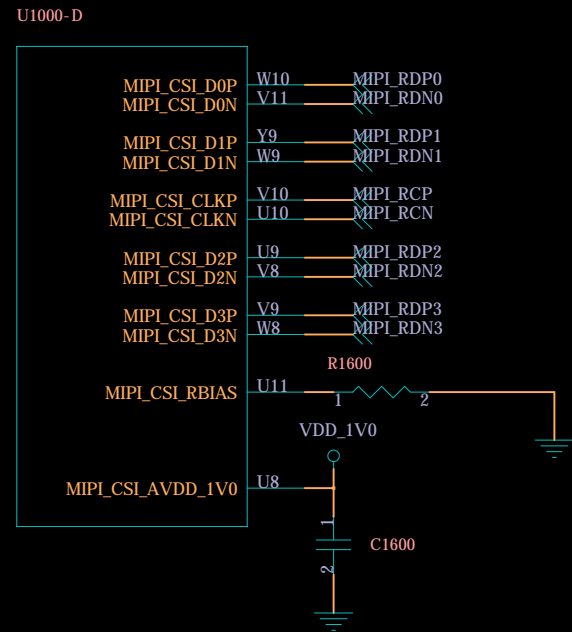
ADC0_HW_ID	Pull-up Resistance	Pull-down Resistance	ADC Value
BOM0(Default)	51K	DNP	1024
BOM1	DNP	51K	0
BOM2	51K	51K	512
BOM3	100K	51K	346
BOM4	51K	100K	678



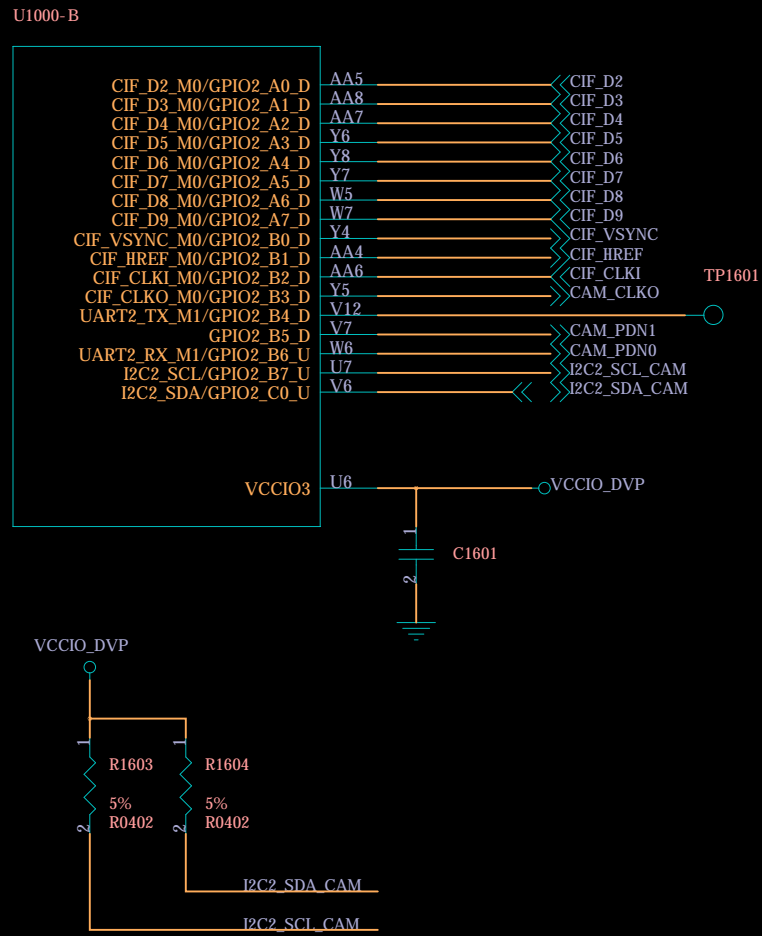
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RK3326 Part-D

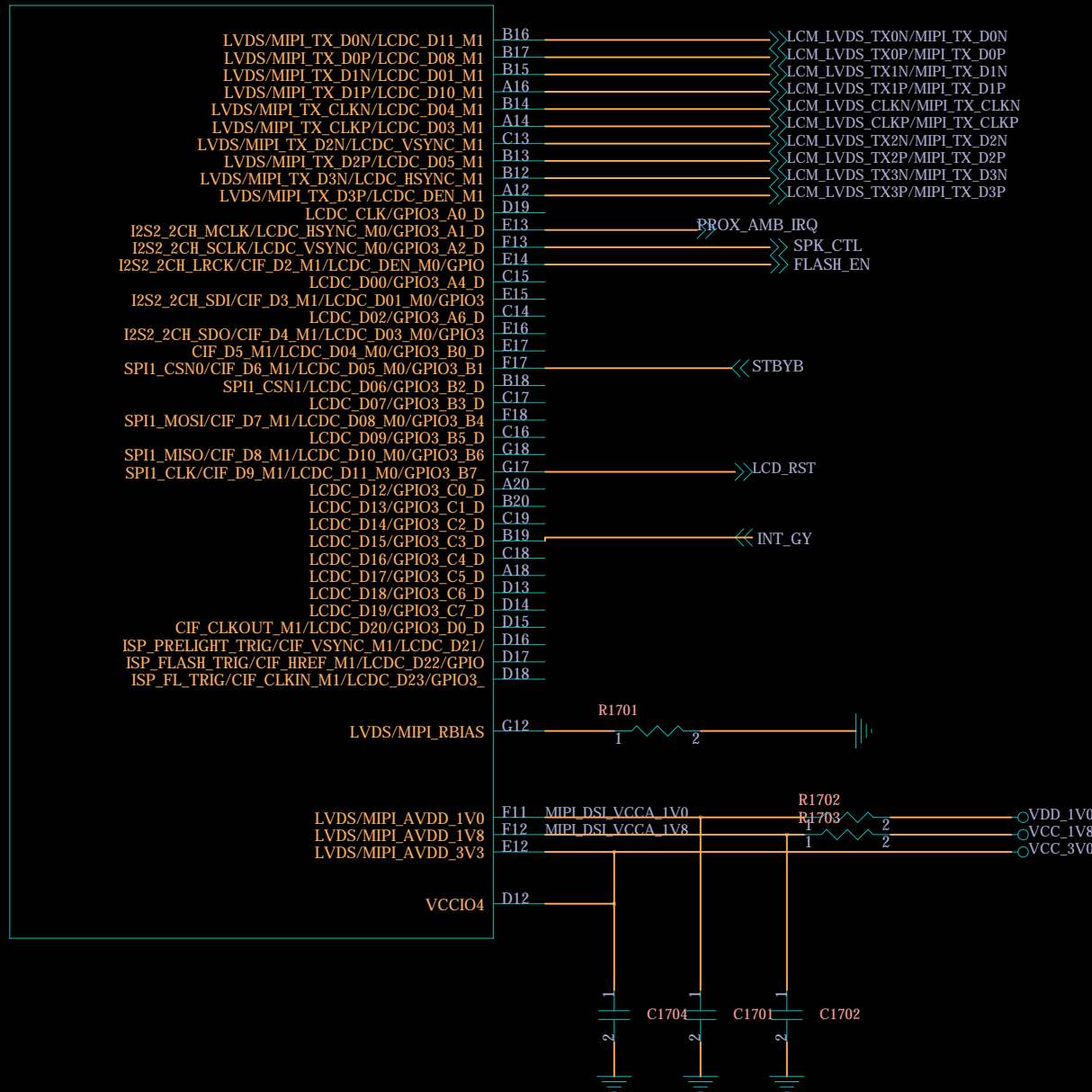


RK3326 Part-B

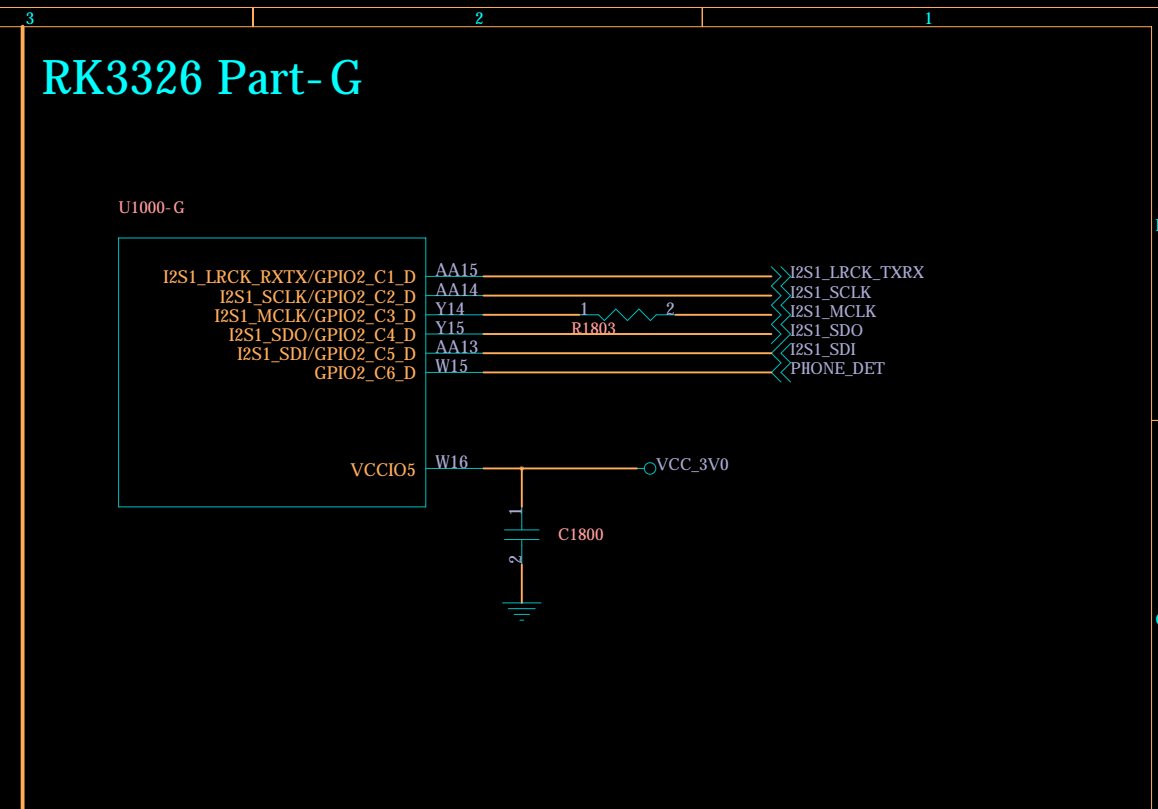
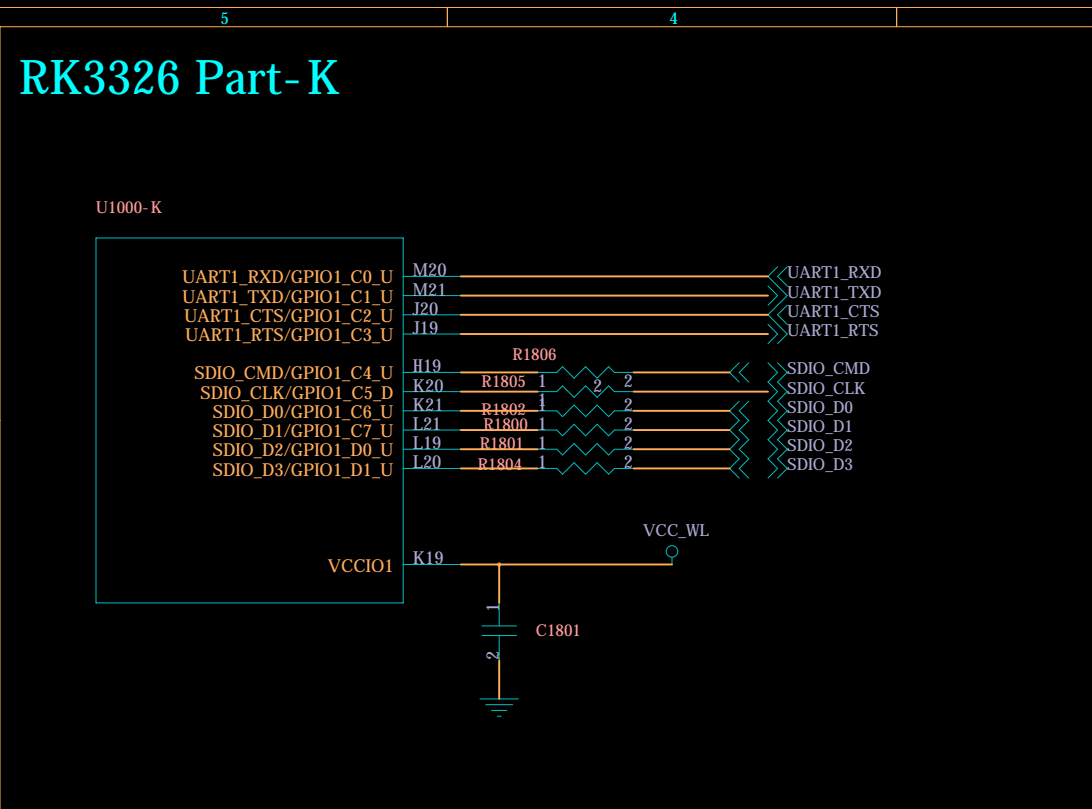


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U1000-M

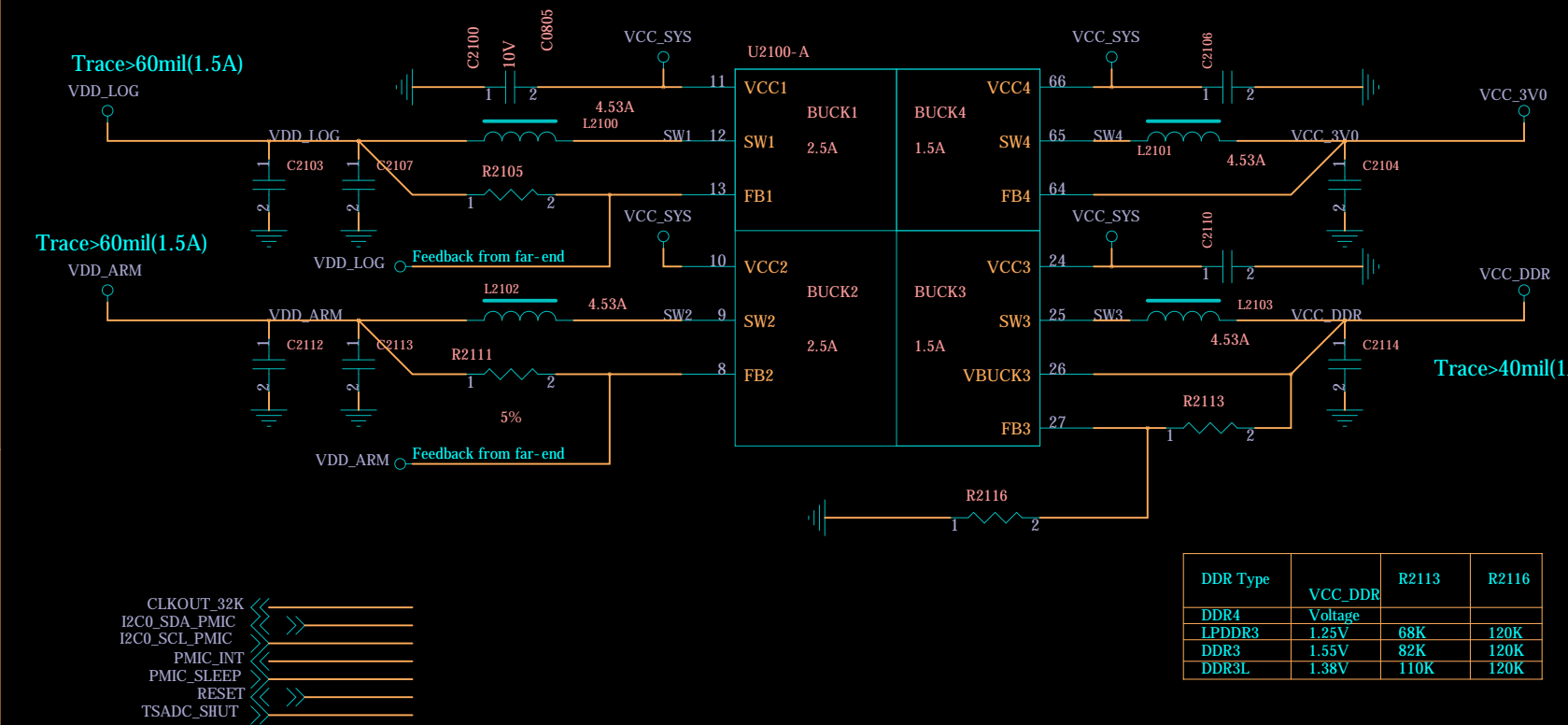


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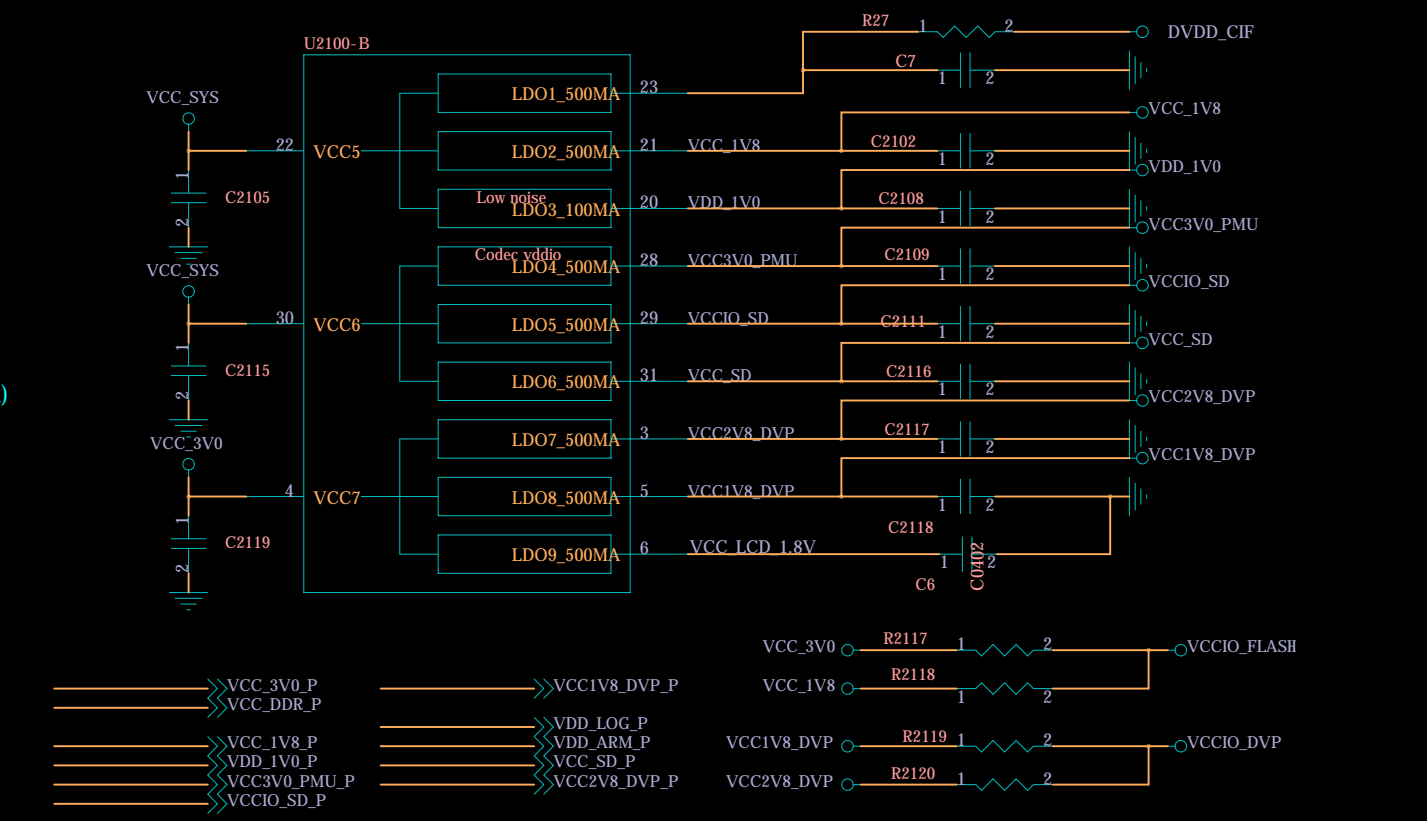


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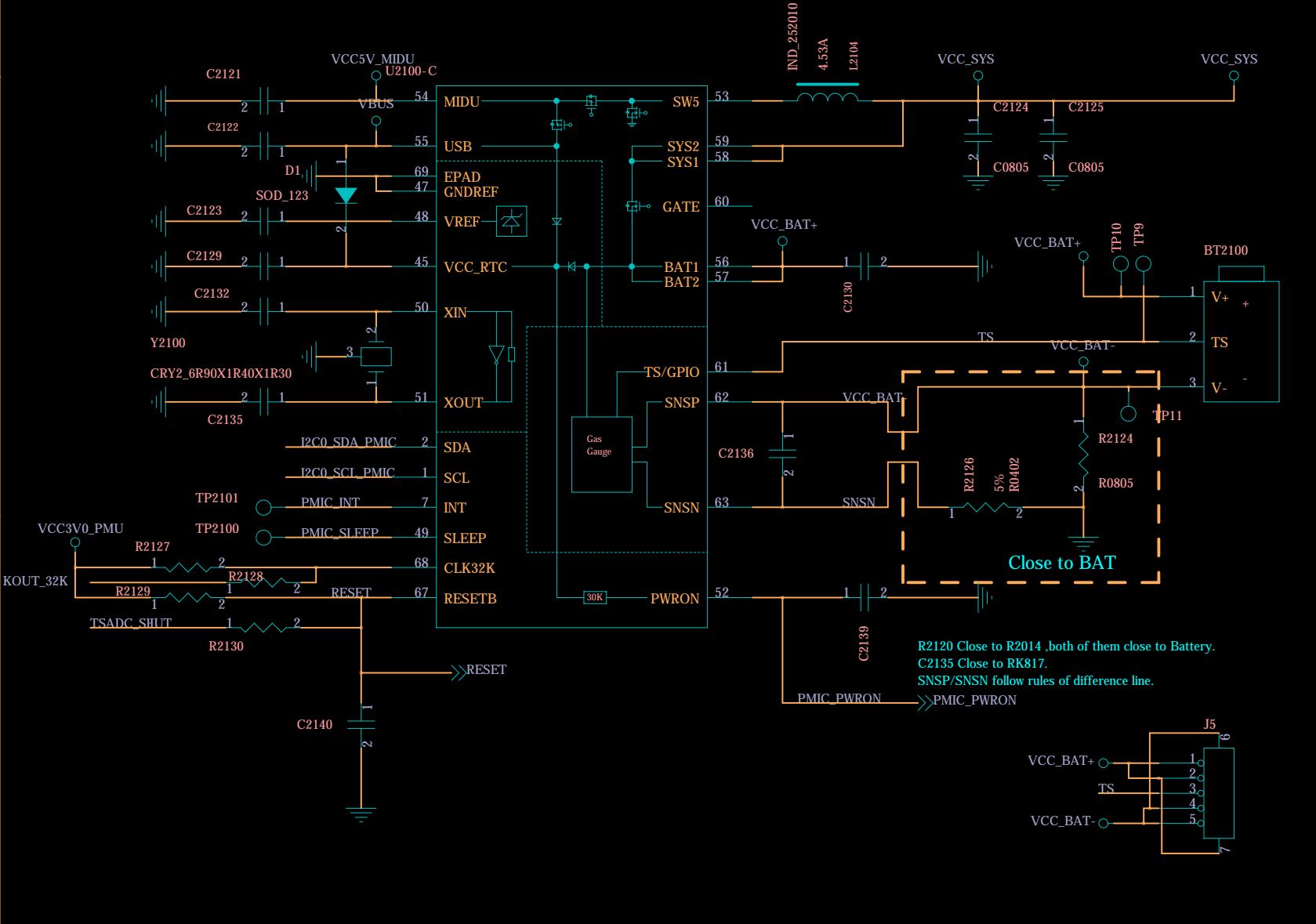
PMIC RK817-1 DCDC



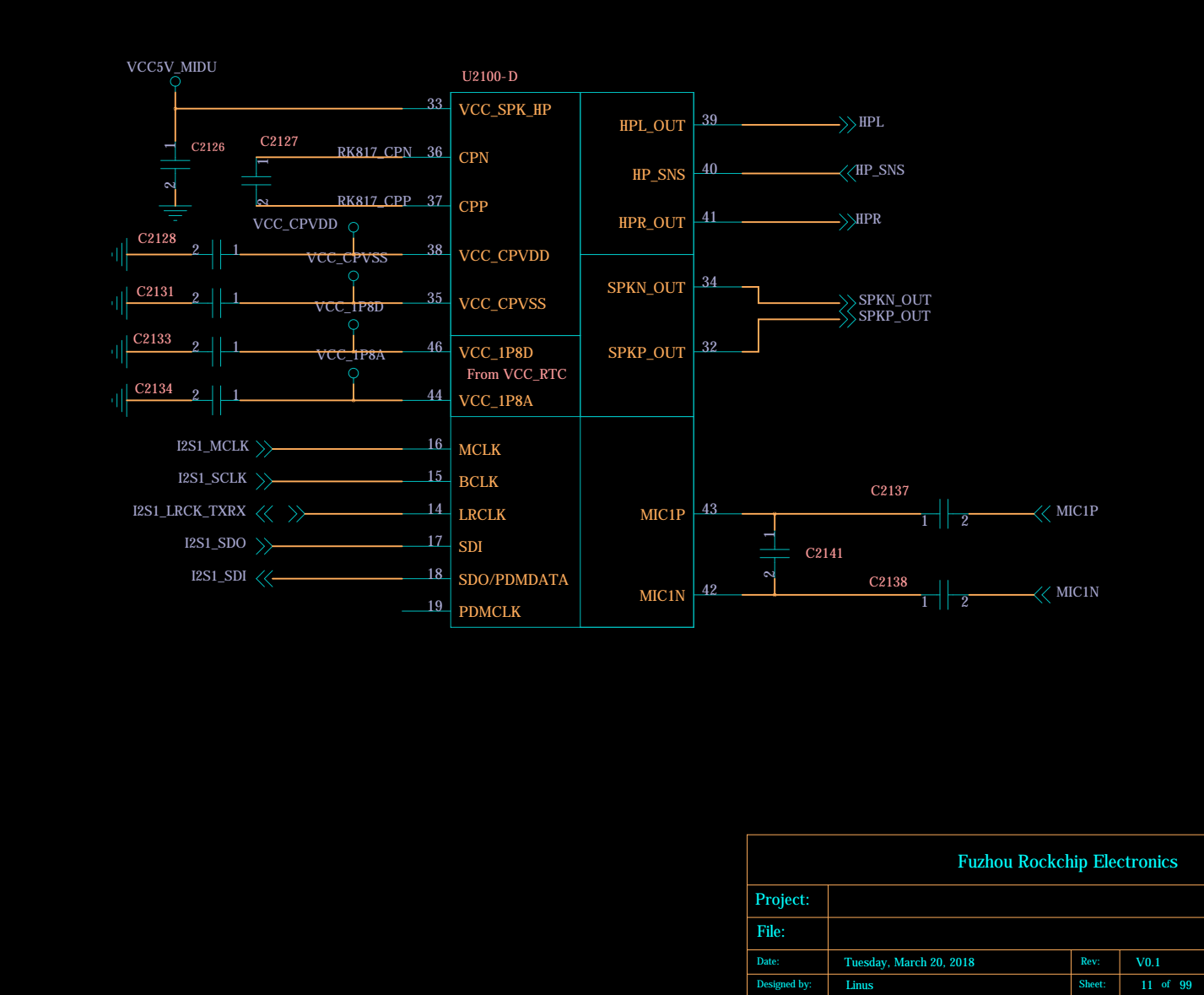
PMIC RK817-1 LDO



PMIC RK817-1 Management



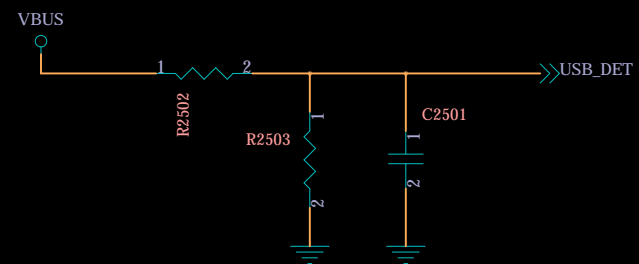
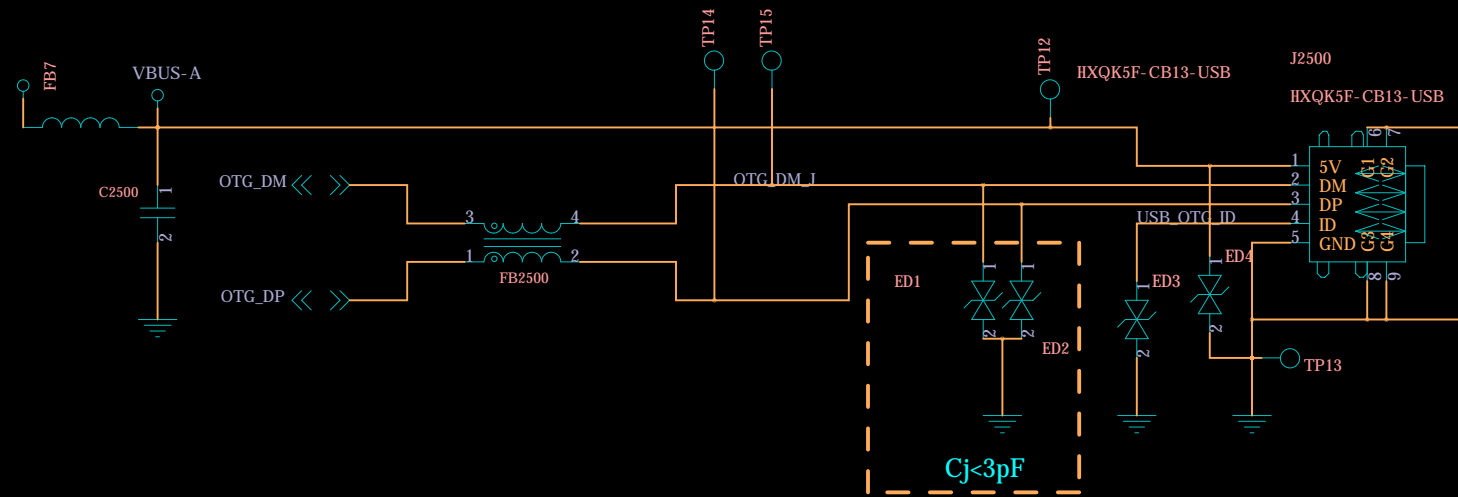
PMIC RK817-1 CODEC



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USB Port

USB_OTG_ID >>



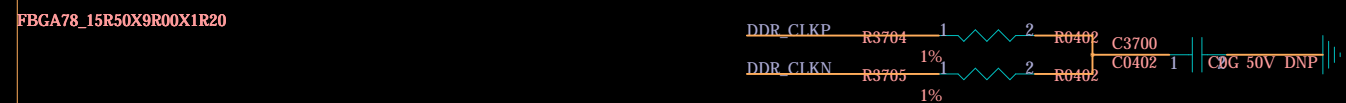
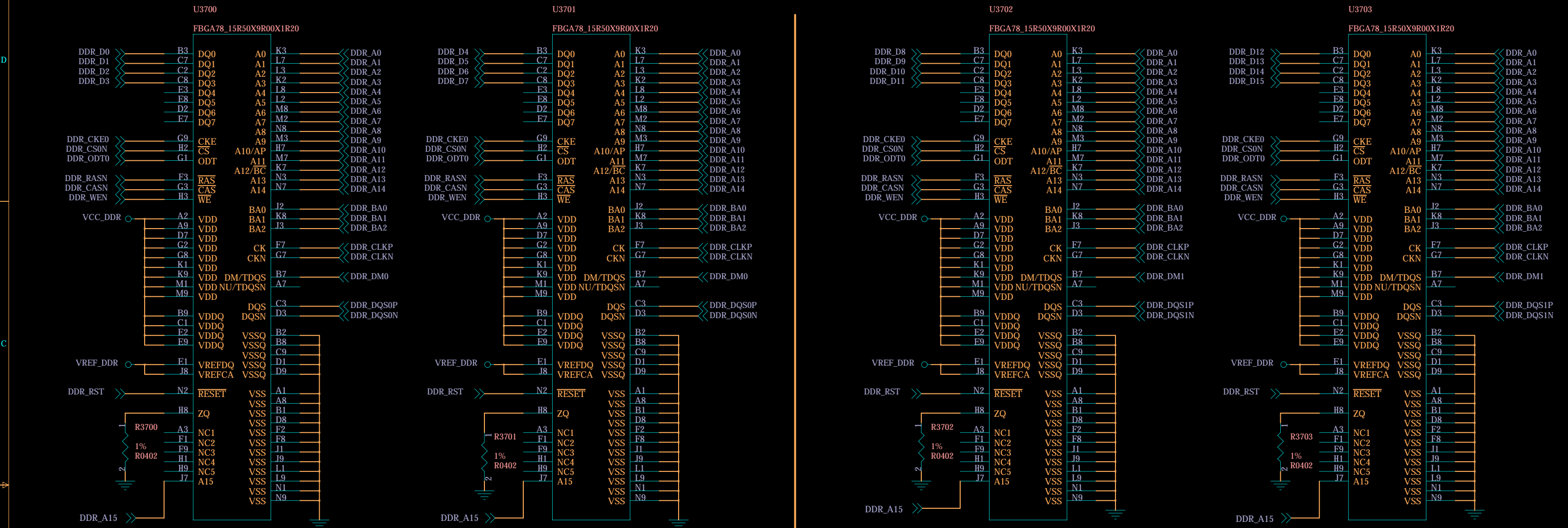
Fuzhou Rockchip Electronics

Project:			
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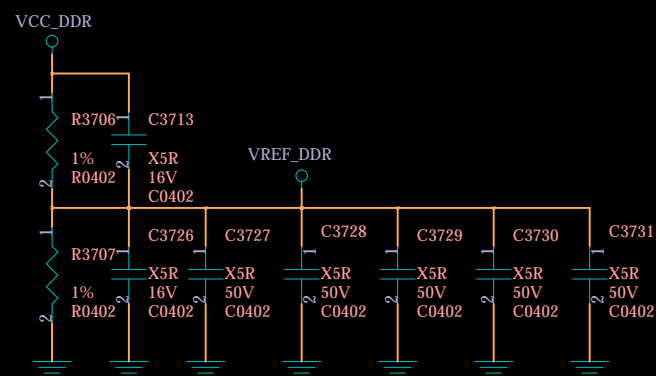
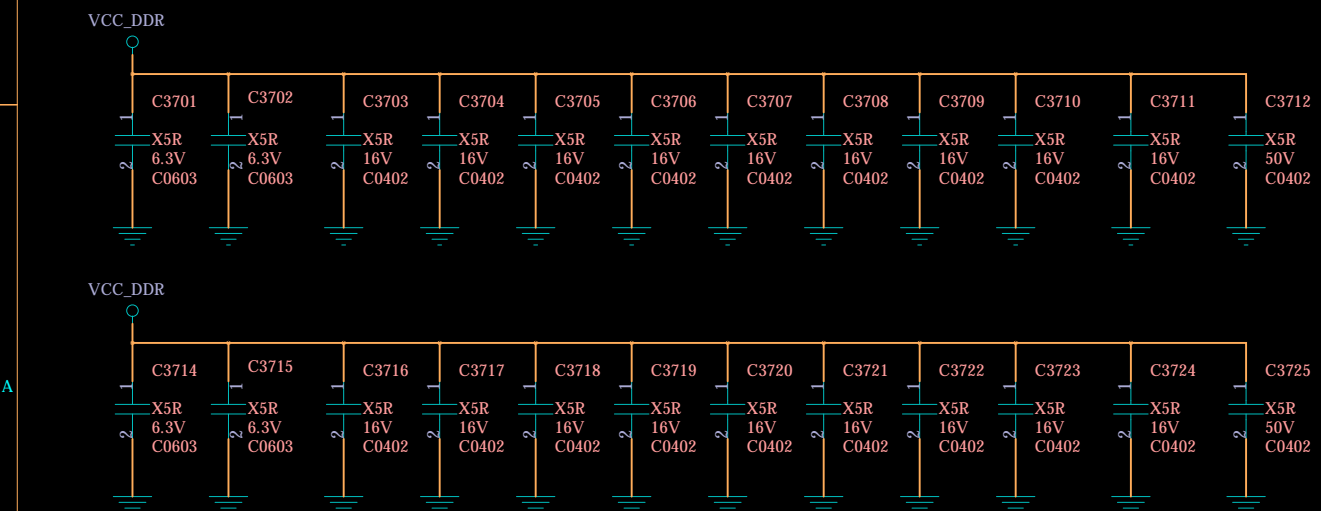
DDR3 4x4bit

4X4bit DDR3

Note: The simulation frequency of the template is 456MHz.
Remind: Refer to the latest AVL for parts selection.



DDR3 FILTER

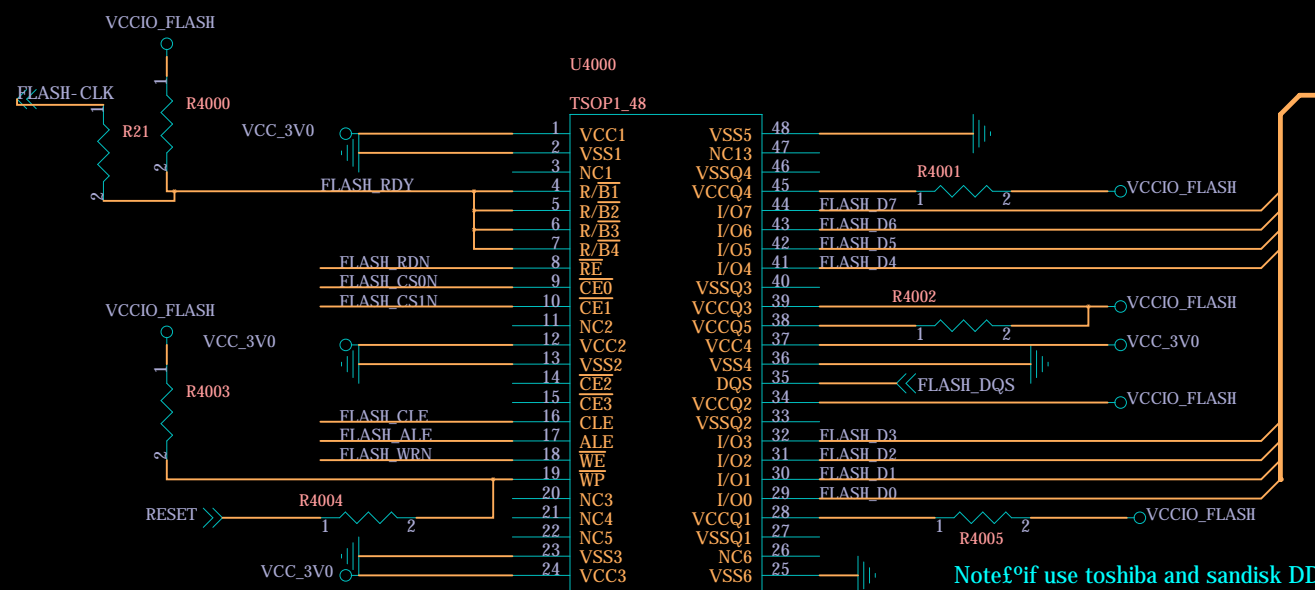
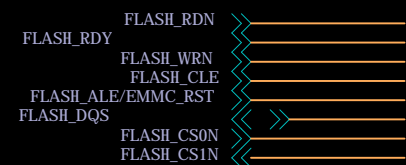


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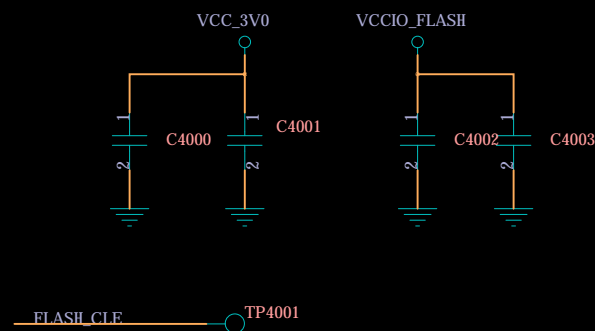
Project:			
File:	RAM DDR3 4x4bit		
Date:	Friday, August 24, 2018	Rev:	V1.0
Designed by:	Zhangdz	Sheet:	11 of 99

Nand Flash

Remind: Refer to the latest AVL for parts selection.



Notef*if use toshiba and sandisk DDR mode, VCCQ1 and VCCQ4 must be connected to VCC_IO;f



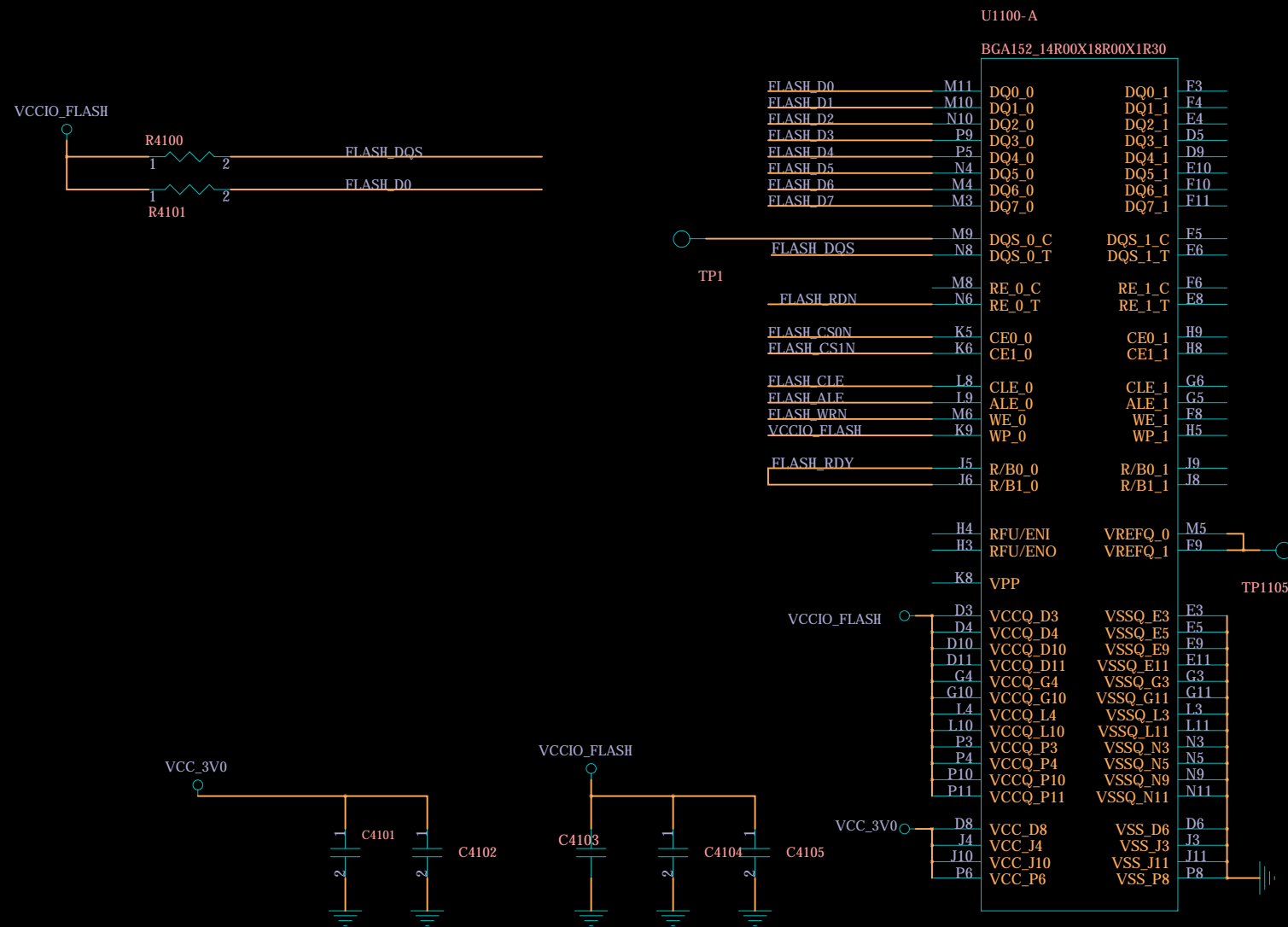
Fuzhou Rockchip Electronics

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File:			
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BGA Flash

Remind: Refer to the latest AVL for parts selection.

FLASH_RDY
FLASH_ALE/EMMC_RST
FLASH_DQS

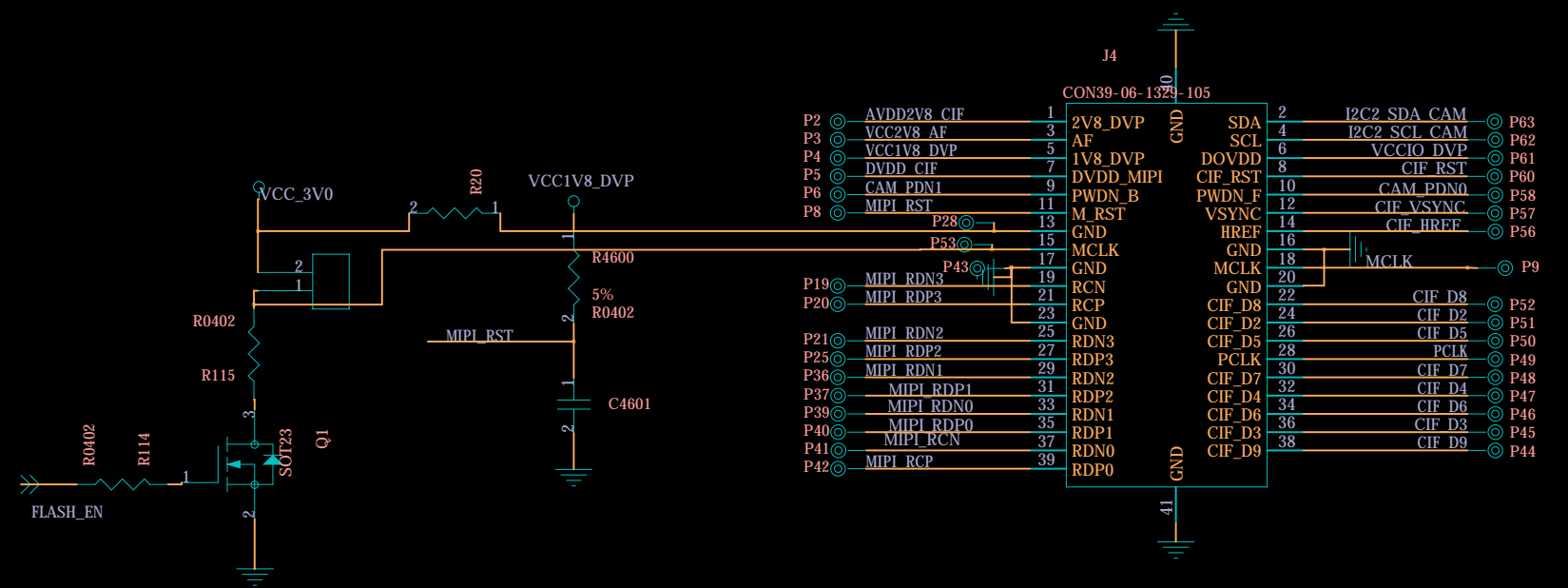
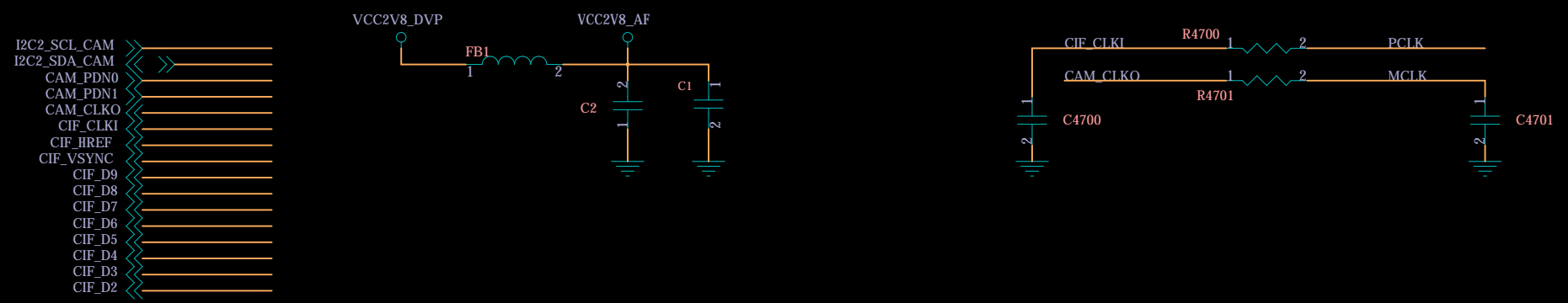


Note:
Reserve TestPoint for firmware update.
If FLASH_CLE=0V at power-on reset,
then system will enter into Maskrom mode.

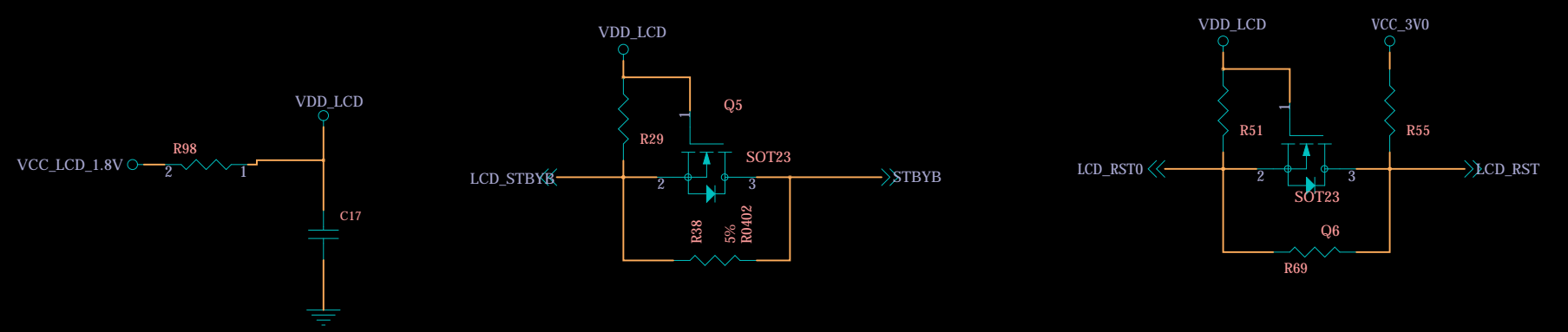
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File:			
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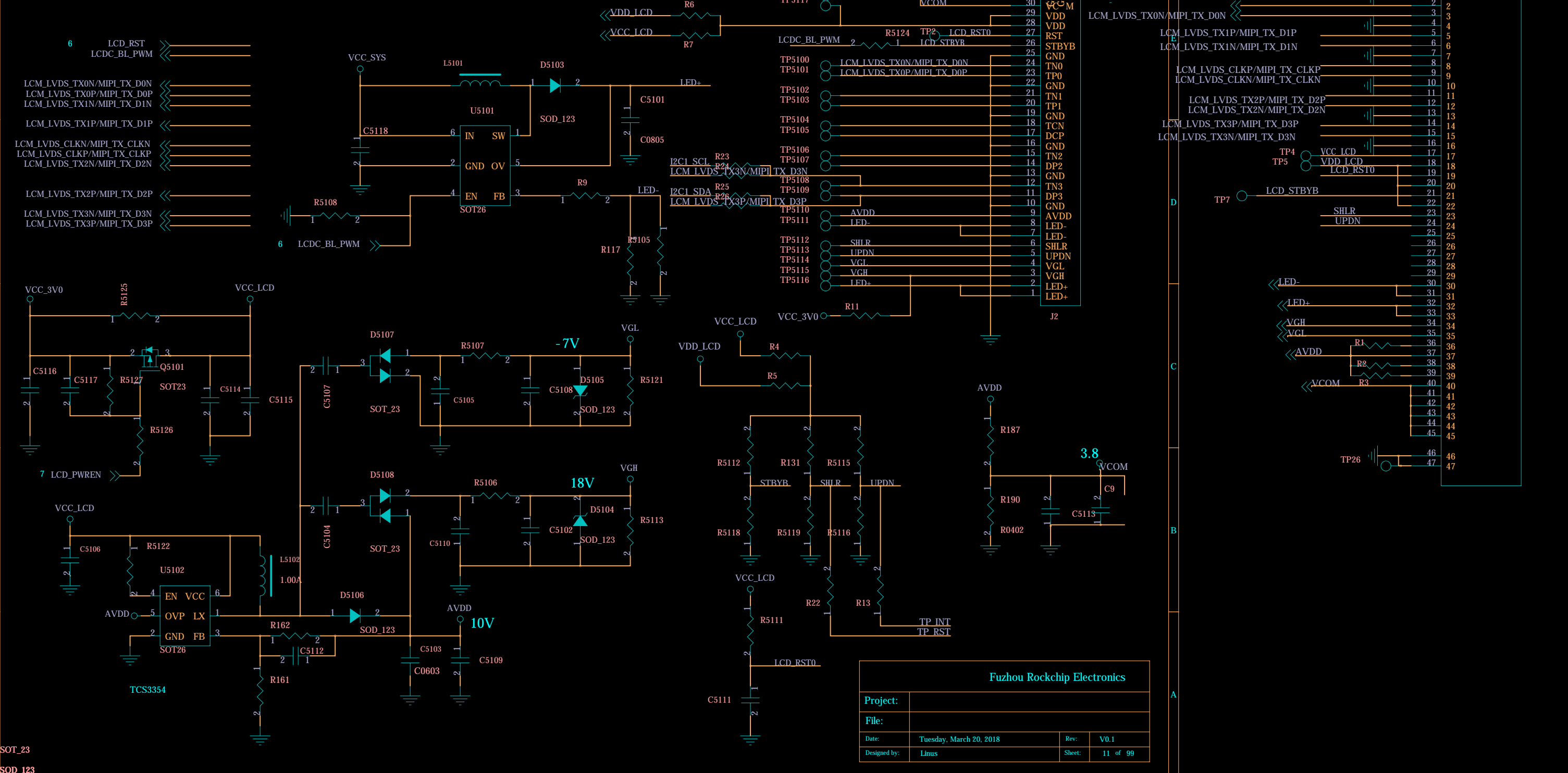
CIF CAMERA



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Project:			
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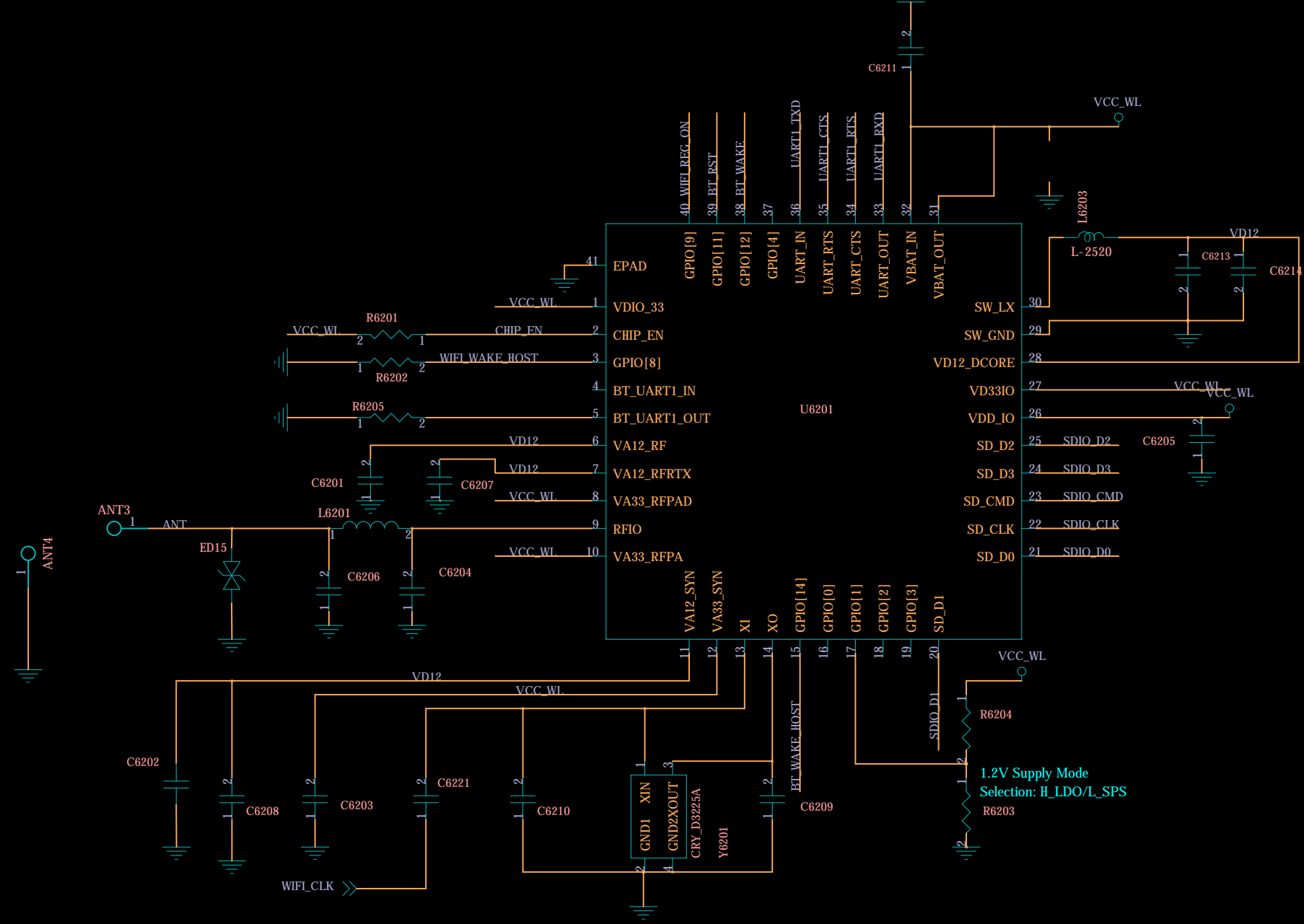
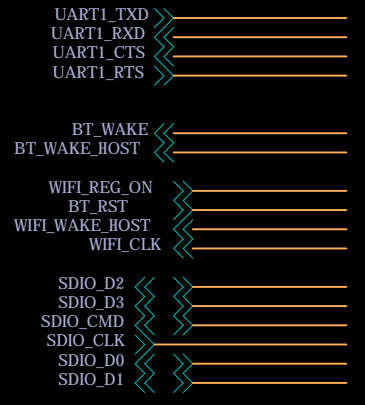


LCM MIPI Connector

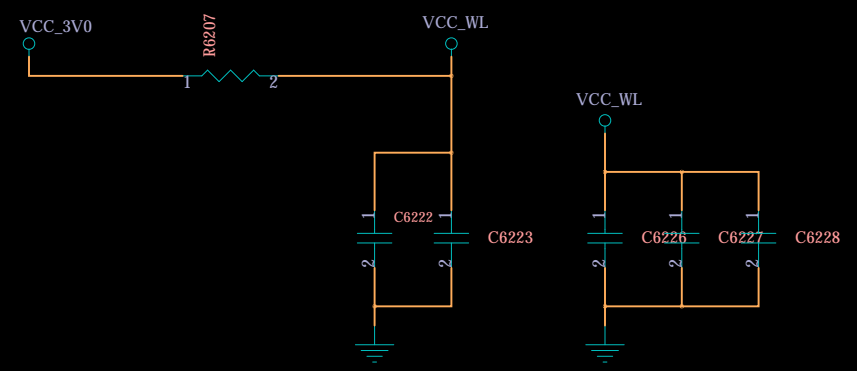


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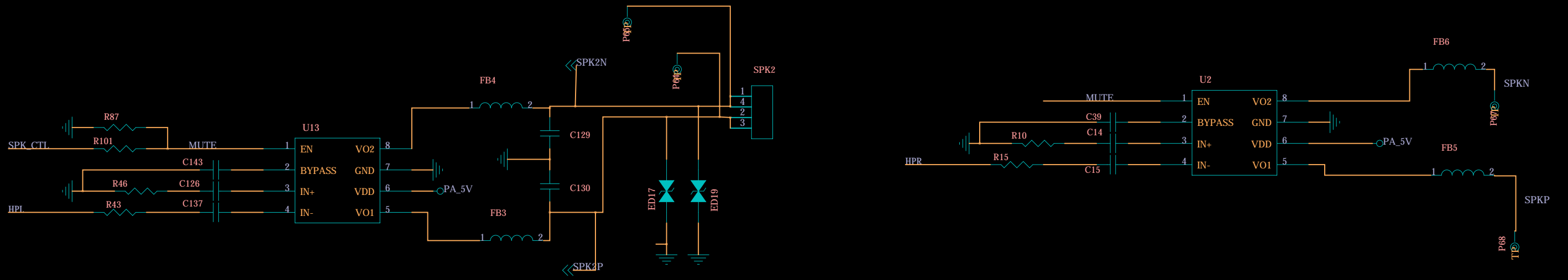
WiFi/BT Module



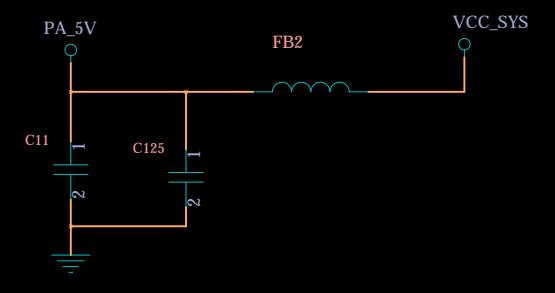
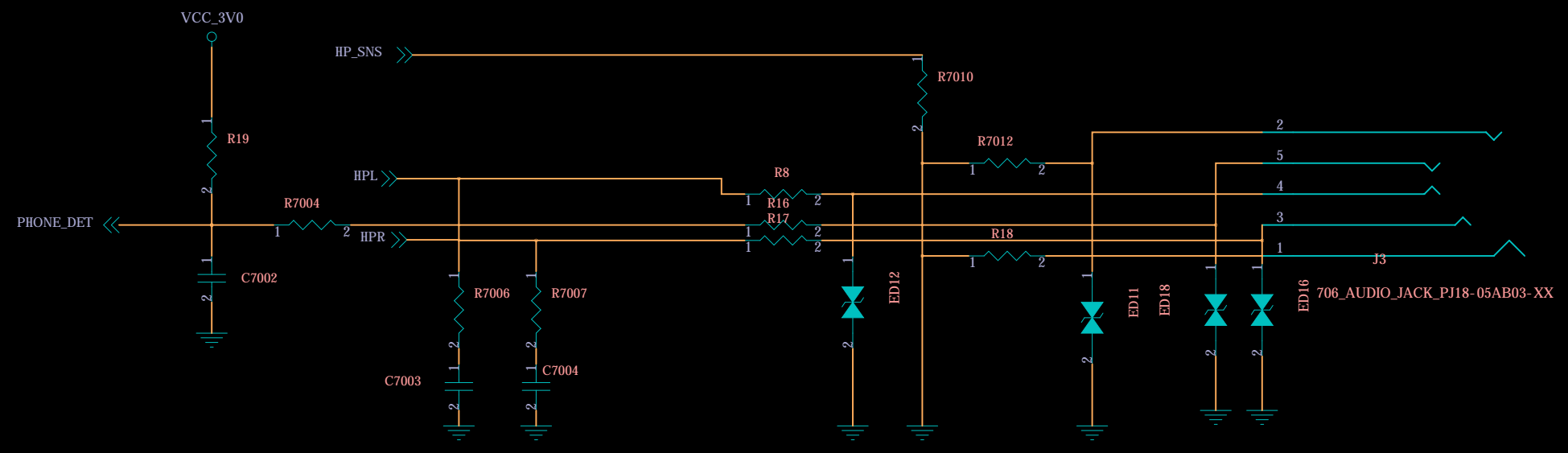
Module Power



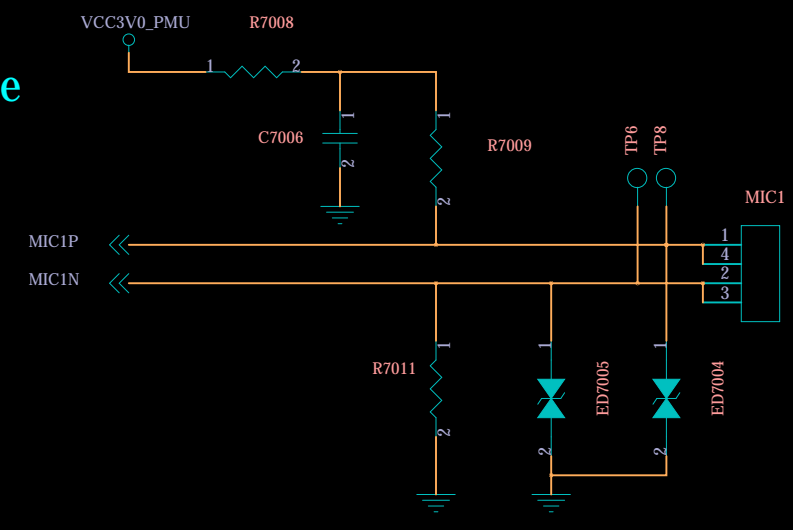
Fuzhou Rockchip Electronics			
Project:			
File:			
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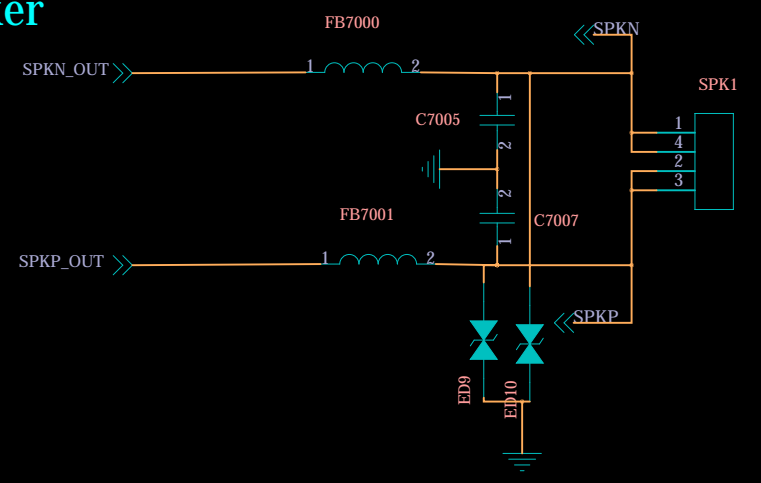
Headphone



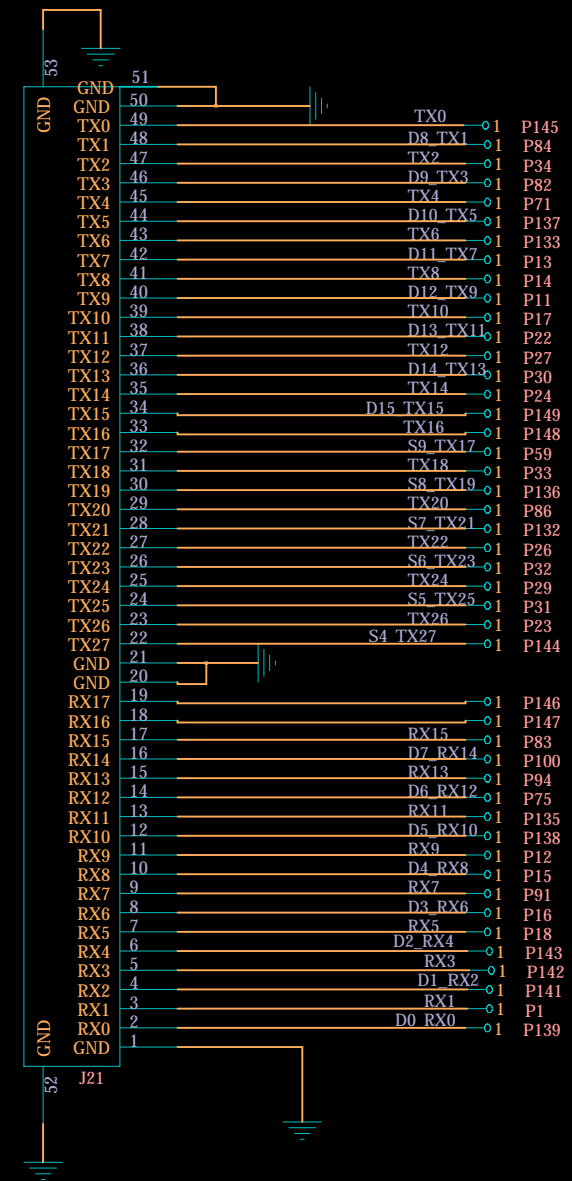
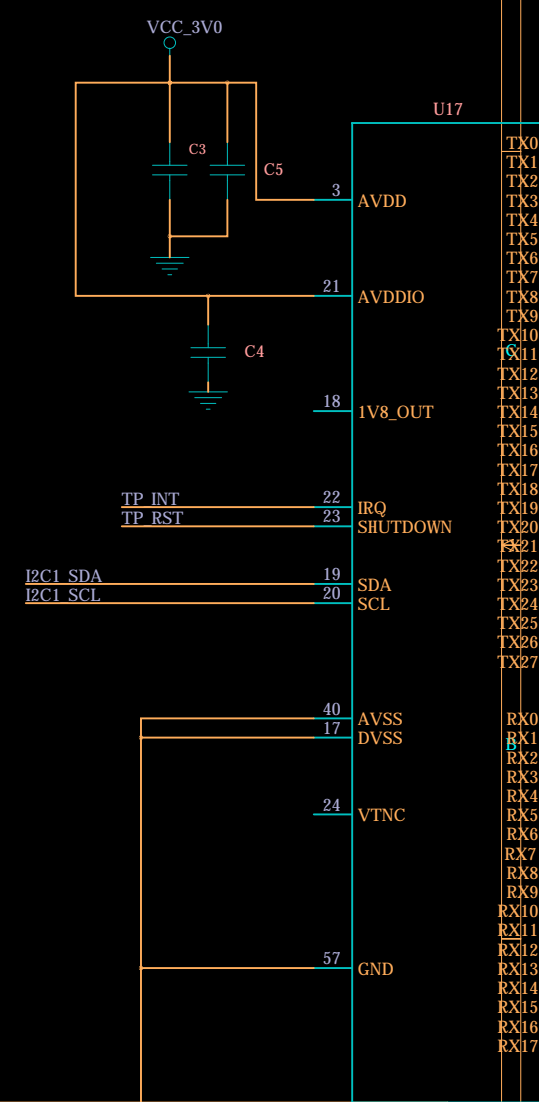
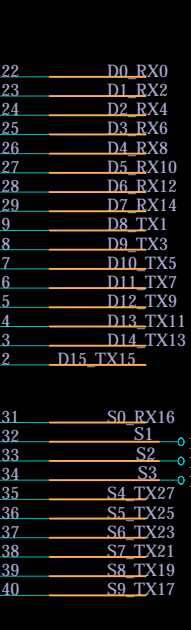
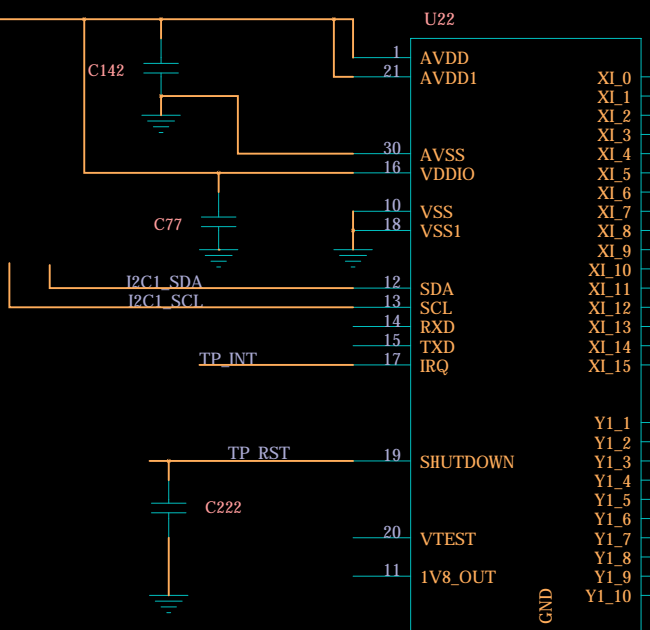
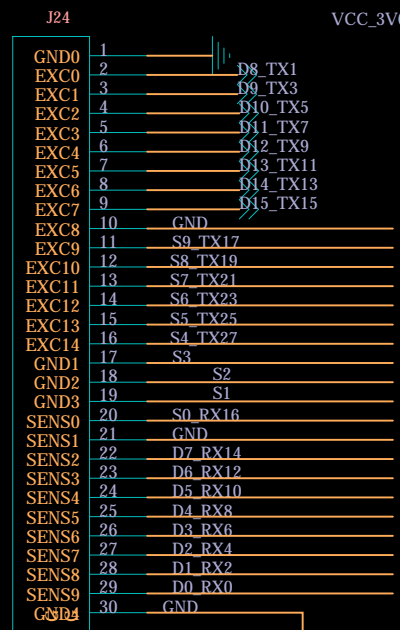
Microphone



Speaker



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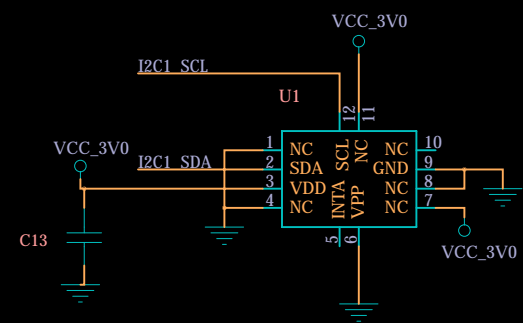
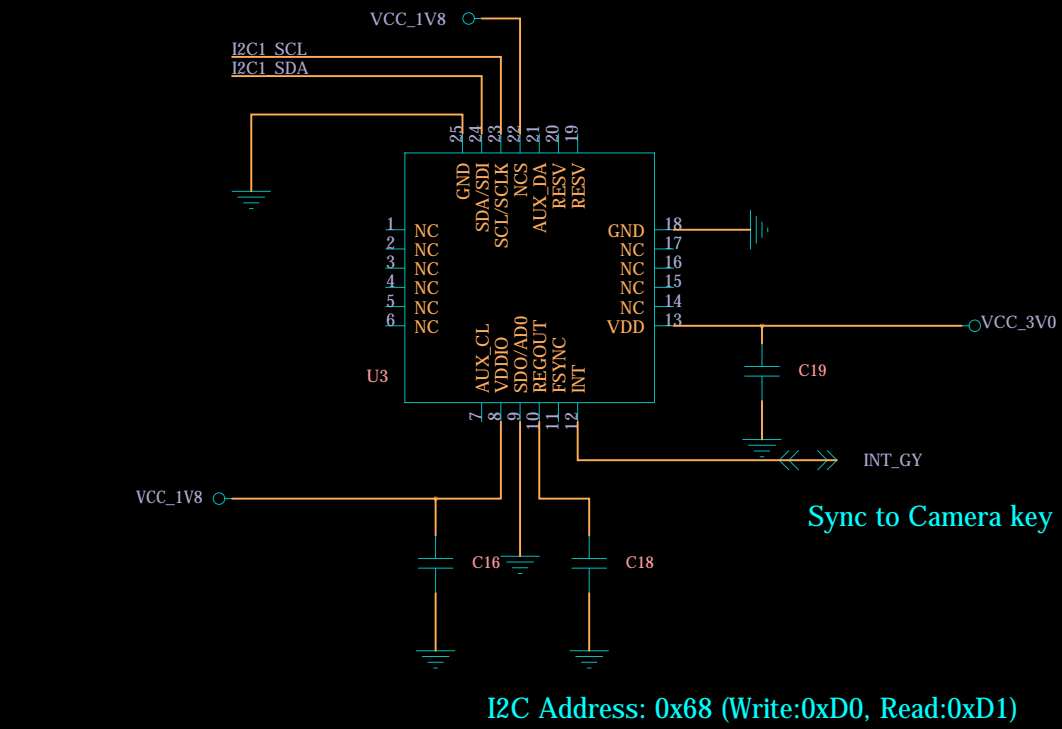
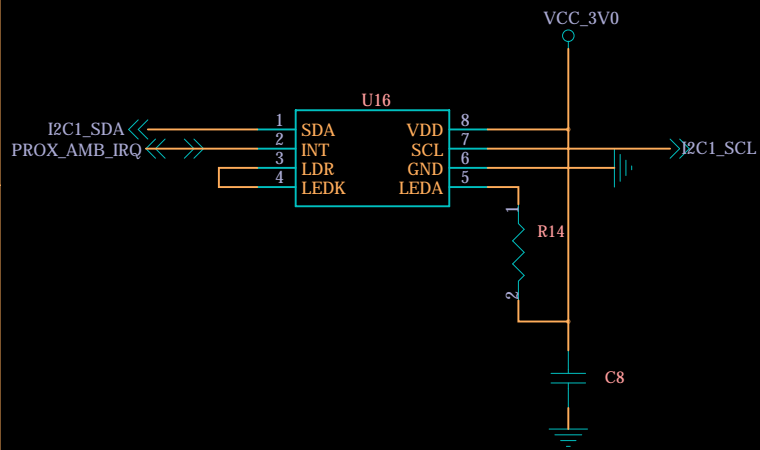
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G-sensor

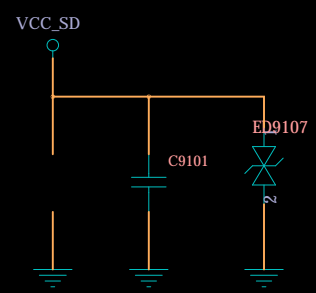
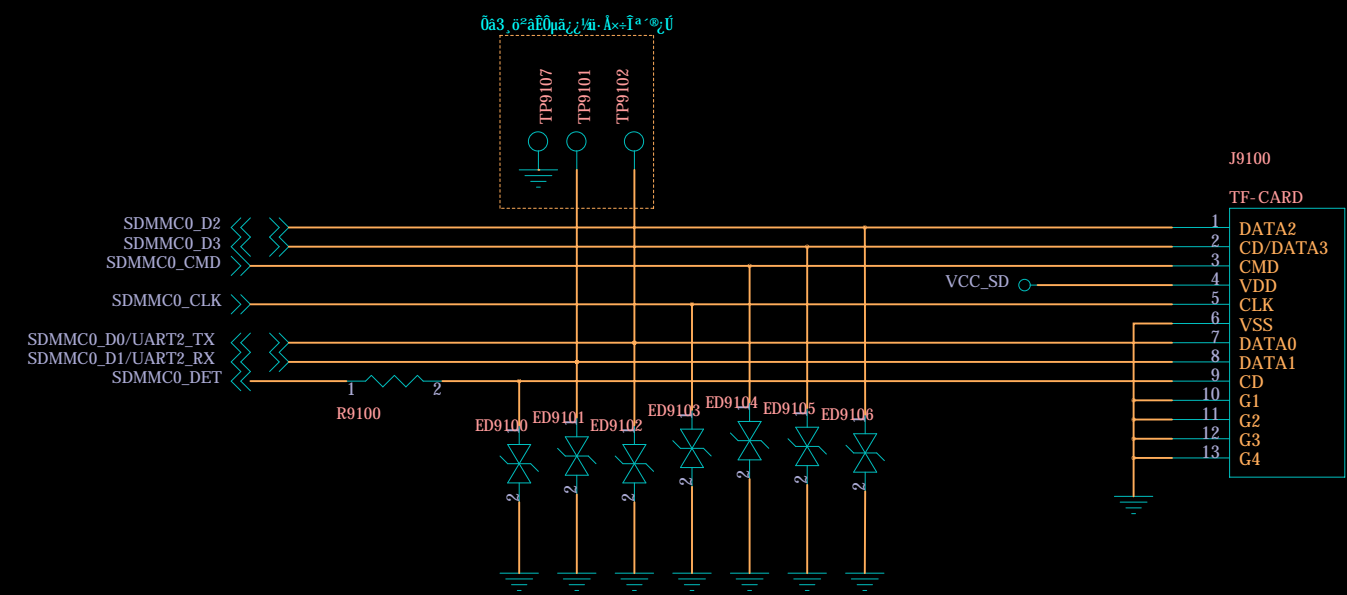
I2C1_SDA
I2C1_SCL
SENSOR_INT

	LIS3DH(A-EI)	MMA8452Q	LSM303D
C9001	NC	NC	4.7uF
R9002	0ohm	NC	NC
R9001	NC	0ohm	NC
C9003	NC	0.1uF	0.22uF
R9003	NC	NC	0R



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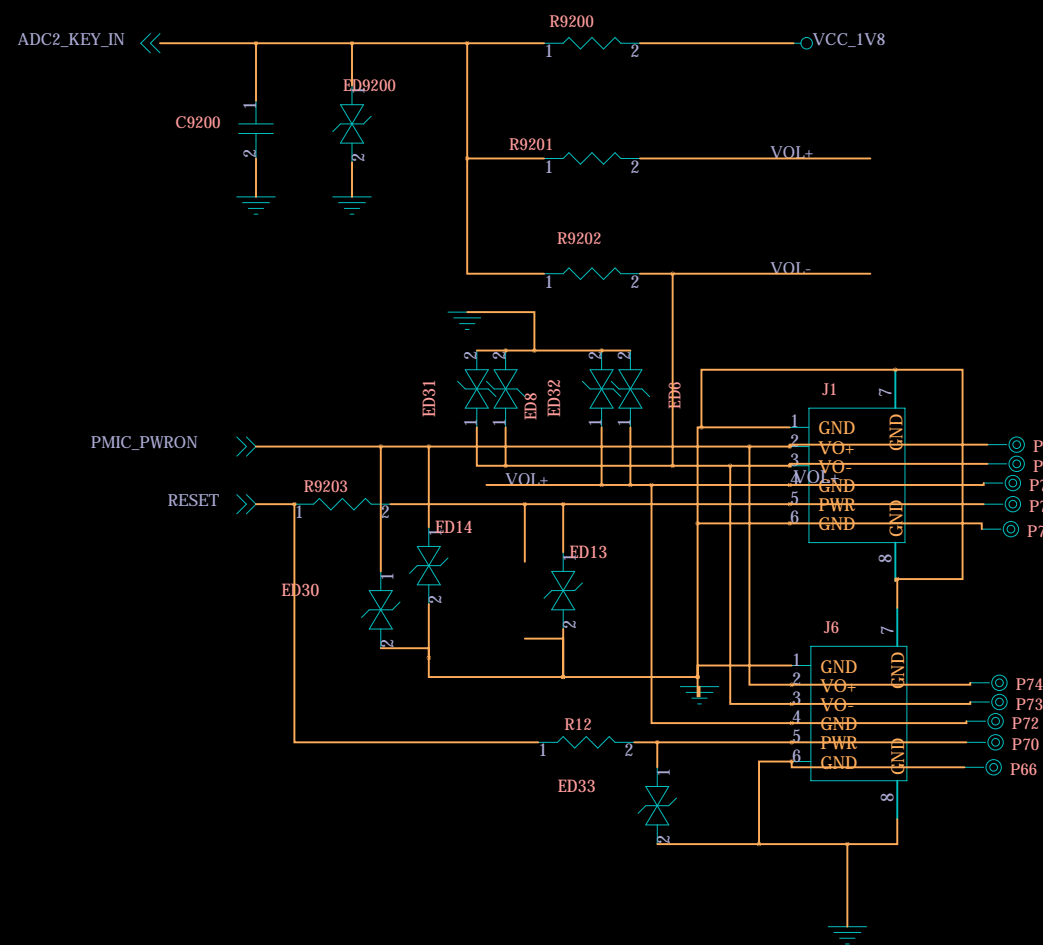
TF Card



Close to TF Connector

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KEY BAORD



Key Name	SARADC
VOL+/RECOVERY	10
VOL-	170

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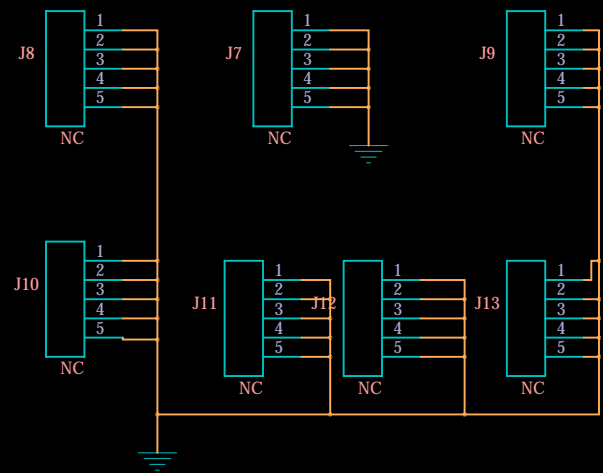
5 4 3 2 1

M9900
MARK

M9903
MARK

M9901
MARK

M9904
MARK



D

C

B

A

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5 4 3 2 1