



COMM	1	PC5/OSC32_IN/[SPI1_NSS]/[USART_CK]/[SWIM2_CH1]/[I2C_SMB/CCO/ADC1_IN4]	20	TMP AD
MM_CTL	2	PC6/OSC32_OUT/[SPI_SCK]/[USART_RX]/TIM2_CH2	19	TMP_CTL
SWIM/CH2	3	PA0/[USART_CK]/SWIM/BEEP/IR_TIM	18	CH IN
NRST	4	NRST/PA1	17	B1
BM_CTL	5	PA2/OSCIN/[USART_TX]/[SPI_MISO]	16	B2
CH_CTL	6	PA3/OSCOUT/[USART_RX]/[SPI_MOSI]	15	B3
	7	VSS/VSSA/VREF-	14	B4
	8	VDD/VDDA/VREF+	13	B5
BM_AD	9	PB3/TIM2_ETR/ADC1_IN15/RTC_ALARM	12	B6
MM_AD	10	PB0/TIM2_CH1/ADC1_IN18	11	B7
		PB1/TIM3_CH1/ADC1_IN17		

STM8L051F3

